



## E-1 ASIC Mainboard

**LEFT: The E-1 mainbord (motherboard) with its now famous 3 ASIC processors.**  
**ASIC: = Application Specific Integrated Circuits.**

**BELOW: Is a schematic showing each processors function; what parts of the image process it is responsible for and how it can be envisaged in the cameras workflow.**

**NOTE: The (then) large 128mb of SDRAM buffer**

